

DESCRIPTION

The module is intended for generating of signals ACF, RES, FAIL for feeding sources of VME bus (IEC 821), which are not equipped with these signals. The signals are generated at rising and falling of guarded voltage levels.

In the module panel there signalled by means of LEDs status +24V, +5V, +12V, -12V, ACF, RES, FAIL.

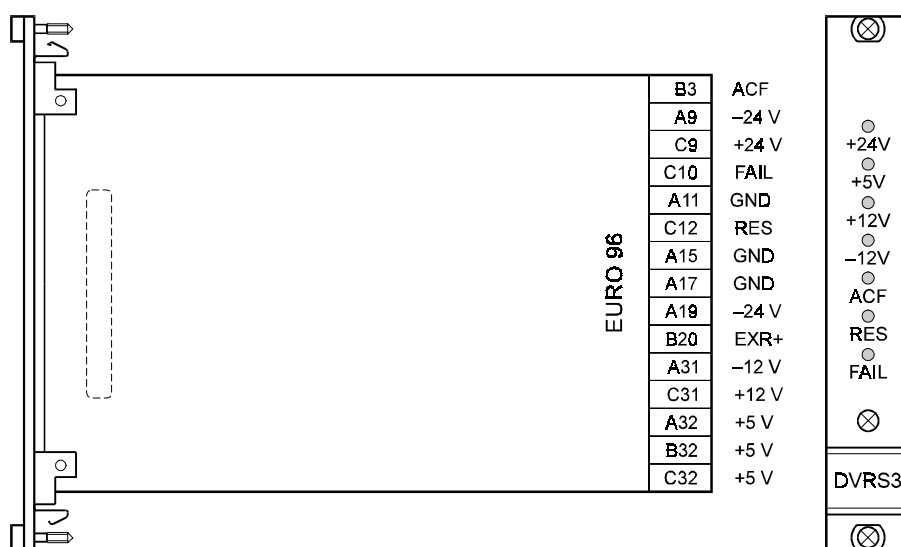


Fig. 1: Front board panel

TECHNICAL DATA

Feeding voltage	5 V, ±12 V, +24 V	Saturation voltage of outputs ACF, RES, FAIL	max. 0,2 V DC
Max. consumption from +24VDC	30 mA	Time interval between rise of +24 V and signal ACF	0,1 up to 0,2 s
Max. consumption from +12 V DC	15 mA	Time interval between rise of ACF and signal RES	0,2 up to 0,3 s
Max. consumption from -12 V DC	15 mA	Time interval between rise of RES and signal FAIL	0,15 up to 0,25 s
Max. consumption from +5 V DC	60 mA	Time interval between fall of +24 V and signal ACF, RES, FAIL	20 up to 50 ms
Decision level for +24 V	17,0 up to 17,9 V DC	Range of working temperatures	-5 °C up to +60 °C
Decision level for +5 V	4,5 up to 4,6 V DC	Weight	0,25 kg
Decision level for +12 V	10,8 up to 11,3 V DC	Dimension	3HE, 4TE
Decision level for -12 V	-10,8 up to -11,5 V DC		