

**Input binary board with simulation and rated voltage 24 VDC**

Boards XE652, XE653 contain 16 galvanic separated binary inputs with simulation switches. Board **XE652** has individual inputs mutually separated; board **XE653** has all the negative poles of binary inputs interconnected. The boards use one address in the system for input operations.

**Function description**

The boards XE652, XE653 transfer input binary signals into BE bus, which sends the signals to control board. The boards contain circuits for galvanic separation of binary signals, filtering of interference pulses and circuits for evaluation of bus control signal and bus driving. The boards transfer maximally 16 input binary signals into BE bus. Input signals voltage is DC one and its polarity has to be kept when connecting it into boards inputs.

16 green LED diodes and 16 simulation switches are placed on the board front panel. LED lighting indicates presence of relevant input or simulated binary signal. When switches are in middle position, the board function is identical with E652, E653. Left switch position simulates logic L level; right switch position simulates logic H level.

**Technical parameters**

Bus

Bus type: BE  
 Feeding voltage: +5 VDC  
 Feeding voltage + 5V tolerance: ± 5%  
 Max. feeding current from + 5V: 250 mA  
 Typical feeding current from + 5V: 200 mA  
 Max. feeding power consumption: 1.3 W

Related equipment

Number of input circuits: 16  
 Input voltage +I against -I: 24 VDC  
 Input voltage + 24V tolerance: ± 25%  
 Current consumption at 24 V: 10 mA  
 Current consumption at 30 V: 12.5 mA  
 Logic H level: ≥17 V  
 Logic L level: ≤ 7 V  
 Max. power consumption of inputs: 6.0 W

Other parameters

Max. power consumption: 7.3 W  
 Operational temperature range: 0°C..60°C  
 Dimension: 6HE 4TE  
 Weight: 0.25 kg

**Boards interface**

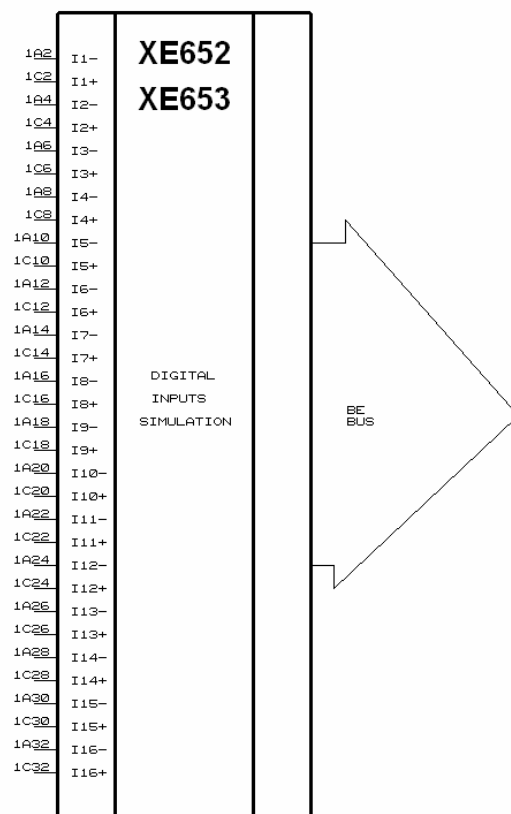
Bus connector

Connection, used signals and their levels comply with requirements of BE bus.

Connector of related equipment

-I - input, negative pole of input signal  
 +I - input, positive pole of input signal

**Interface of boards XE652 ,XE653**



Inputs I1- up to I16- of board XE653 (ie. pins 1A2 up to 1A32) are mutually interconnected.

This page is intentionally left blank.